

```

graph LR
    120[Central Controller] --- 110[BACKPLANE]
    110 --- 130_1[Line Card 1]
    110 --- 130_N[Line Card N]
    130_1 --- 140_1[Physical Card 1]
    130_N --- 140_N[Physical Card N]

```

The diagram illustrates a network architecture. A Central Controller (120) is connected to a Backplane (110). The Backplane (110) is connected to multiple Line Cards (130, 130N). Each Line Card (130, 130N) is connected to a corresponding Physical Card (140, 140N).

100

The diagram illustrates a multi-line card architecture. It consists of two vertical blocks representing 'Fabric Slot 1' and 'Fabric Slot N'. Each slot is connected to a corresponding 'Line Card' (Line Card 1 and Line Card N). The connections are as follows:

- Fabric Slot 1:**
 - Label: 110
 - Contains 'Fabric Slot 1'.
 - Connected to 'Line Card 1' via two 'Fabric Ports' labeled 1 and 2.
- Fabric Slot N:**
 - Label: 130_N
 - Contains 'Fabric Slot N'.
 - Connected to 'Line Card N' via two 'Fabric Ports' labeled 1 and 2.
- Line Card 1:**
 - Label: 130₁
 - Contains 'Line Card 1'.
 - Has four 'Slot Ports' labeled 1, 2, 3, and 4.
- Line Card N:**
 - Label: 130_N
 - Contains 'Line Card N'.
 - Has four 'Slot Ports' labeled 1, 2, 3, and 4.

FIG. 2.

130

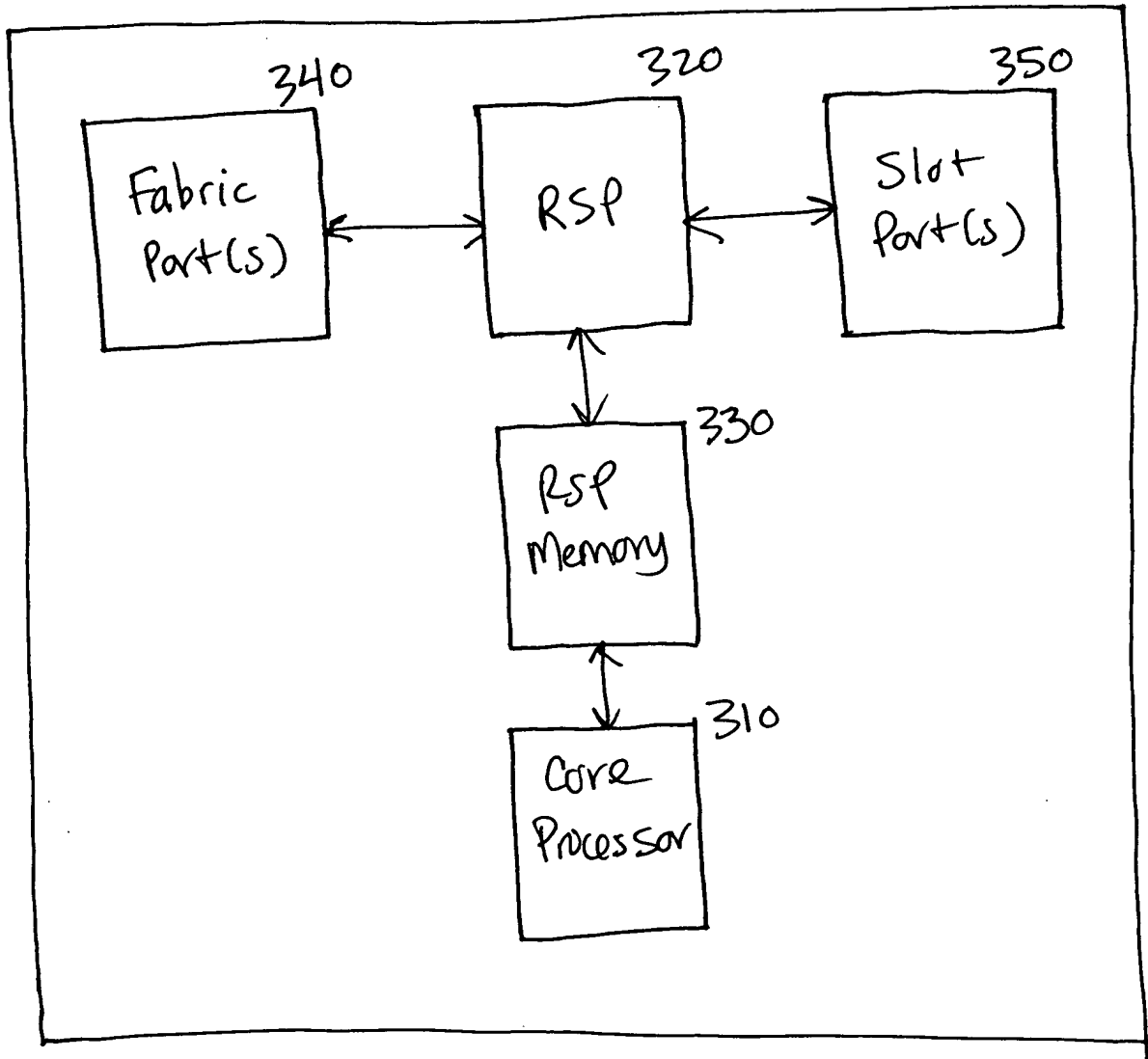


FIG. 3

(S1,G1) \dashrightarrow 1,5,7,9,B,C 410

(S2,G2) \dashrightarrow 5,7,9 420

(S3,G3) \dashrightarrow 1,5,B,C 430

(S2,G2) \rightarrow 5,7,9 420

$(S3, G3) \dashrightarrow 1, 5, B, C$ 430

400

Diagram illustrating two mappings from a large box to smaller boxes:

- Top mapping: $(S1, G1)$ maps to $1, 2, 5, 7, 9, B, C$ with a value of 510.
- Bottom mapping: $(S3, G3)$ maps to $1, 2, 5, B, C$ with a value of 530.

Diagram illustrating two mappings from a large box to smaller boxes:

- Top mapping: $(S1, G1)$ maps to $1, 2, 5, 7, 9, B, C$ with a value of 510.
- Bottom mapping: $(S3, G3)$ maps to $1, 2, 5, B, C$ with a value of 530.

500

SLE	SLD	SLC	SLB	SLA	SL9	SL8	SL7	SL6	SL5	SL4	SL3	SL2	SL1

FIG. 6B - Bridged Routing Vector [Slot5,Slot6]

640

096624.096624

Route 1

(S1,G1)

1,5,7,8,9,B,C

710

Route 2

(S2,G2)

5,7,8,9

720

FIG. 7A - Bridged Routing Table [Slot7,Slot8]

700

Diagram illustrating the mapping of input pairs to output sets:

- Input pair $(S1, G1)$ maps to the set $\{1, 5, 7, 9, A, B, C\}$ with a value of 810.
- Input pair $(S2, G2)$ maps to the set $\{5, 7, 9, A\}$ with a value of 820.

Diagram illustrating the mapping of input pairs to output sets:

- Input pair $(S1, G1)$ maps to the set $\{1, 5, 7, 9, A, B, C\}$ with a value of 810.
- Input pair $(S2, G2)$ maps to the set $\{5, 7, 9, A\}$ with a value of 820.

800

(S1,G1) → 1,5,7,9,B,C,E 910

(S3,G3) → 1,5,B,C,E 930

(S1,G1) → 1,5,7,9,B,C,E 910

(S3,G3) → 1,5,B,C,E 930

900

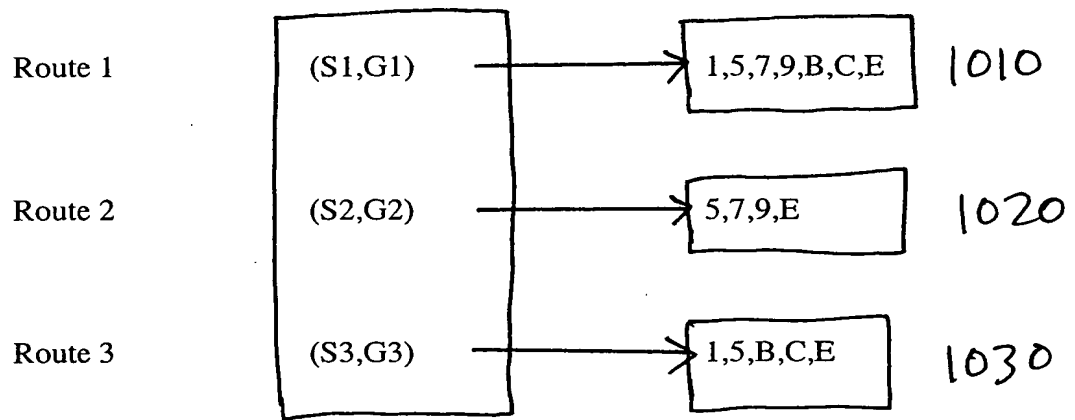


FIG. 10A - Bridged Routing Table [Slot5,SlotE]

1000

002200 01255900

Route 1

(S1,G1)

1,5,7,9,B,C,E

1110

Route 2

(S2,G2)

5,7,9,E

1120

FIG. 11A - Bridged Routing Table [Slot7,SlotE]

1100

SLE	SLD	SLC	SLB	SLA	SL9	SL8	SL7	SL6	SL5	SL4	SL3	SL2	SL1

FIG. 11B - Bridged Routing Vector [Slot7,SlotE]

1140

002200-6126960

Diagram illustrating the mapping of input pairs to output sets:

- Input pair $(S1, G1)$ maps to the set $\{5, 7, 9, B, C, E\}$ with value 1210.
- Input pair $(S2, G2)$ maps to the set $\{5, 7, 9, E\}$ with value 1220.

(S2,G2) → 5,7,9,E 1220

1200

[illegible]

The diagram shows a vertical list of 16 slots on the left, enclosed in a large rectangle. The slots are labeled Slot 1 through Slot E. On the right, there are four rectangular boxes representing address ranges: 1310, 1320, 1330, and 1340. Horizontal lines connect the slots to these ranges: Slot 1 to 1310, Slot 5 to 1320, Slot 7 to 1330, and Slot 9 to 1340. Slots 2, 3, 4, 6, 8, A, B, C, D, and E are not connected to any range.

Slot	Address Range
Slot 1	1310
Slot 2	
Slot 3	
Slot 4	
Slot 5	1320
Slot 6	
Slot 7	1330
Slot 8	
Slot 9	1340
Slot A	
Slot B	
Slot C	
Slot D	
Slot E	

1300

002260 01289900

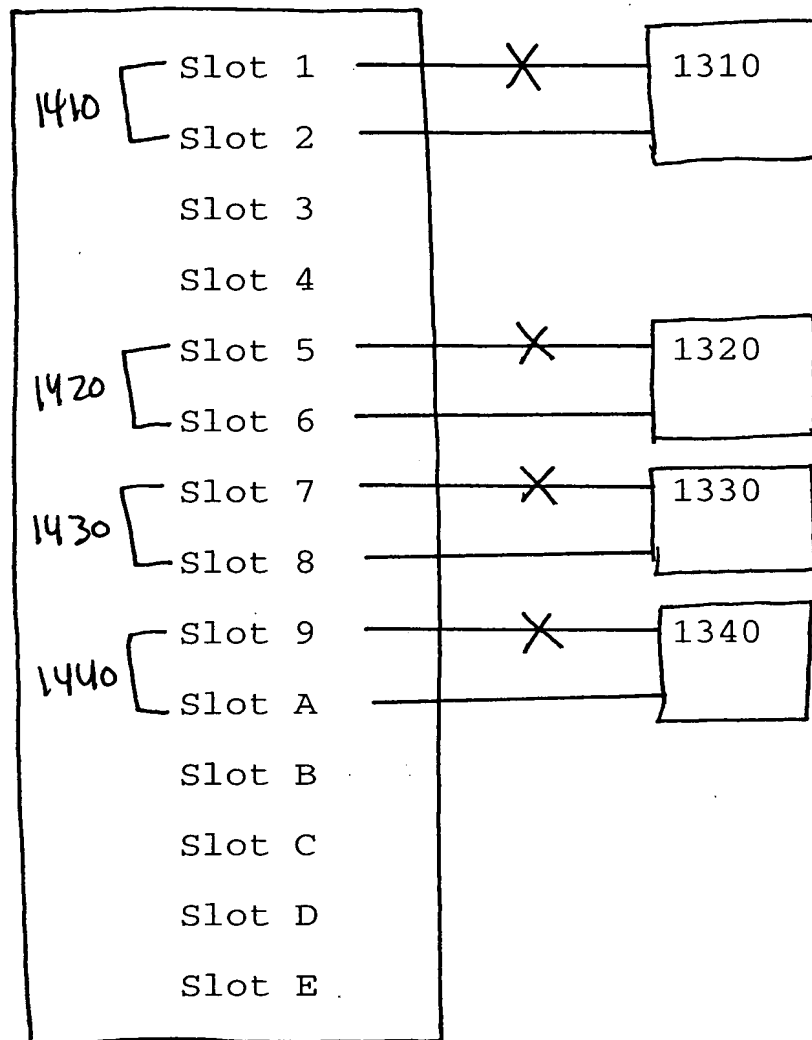


FIG. 14

1400

Slot 1

Slot 2

Slot 3

Slot 4

Slot 5

Slot 6

Slot 7

Slot 8

Slot 9

Slot A

Slot B

Slot C

Slot D

Slot E

1310

1320

1330

1340

1510

X

FIG. 15 1500

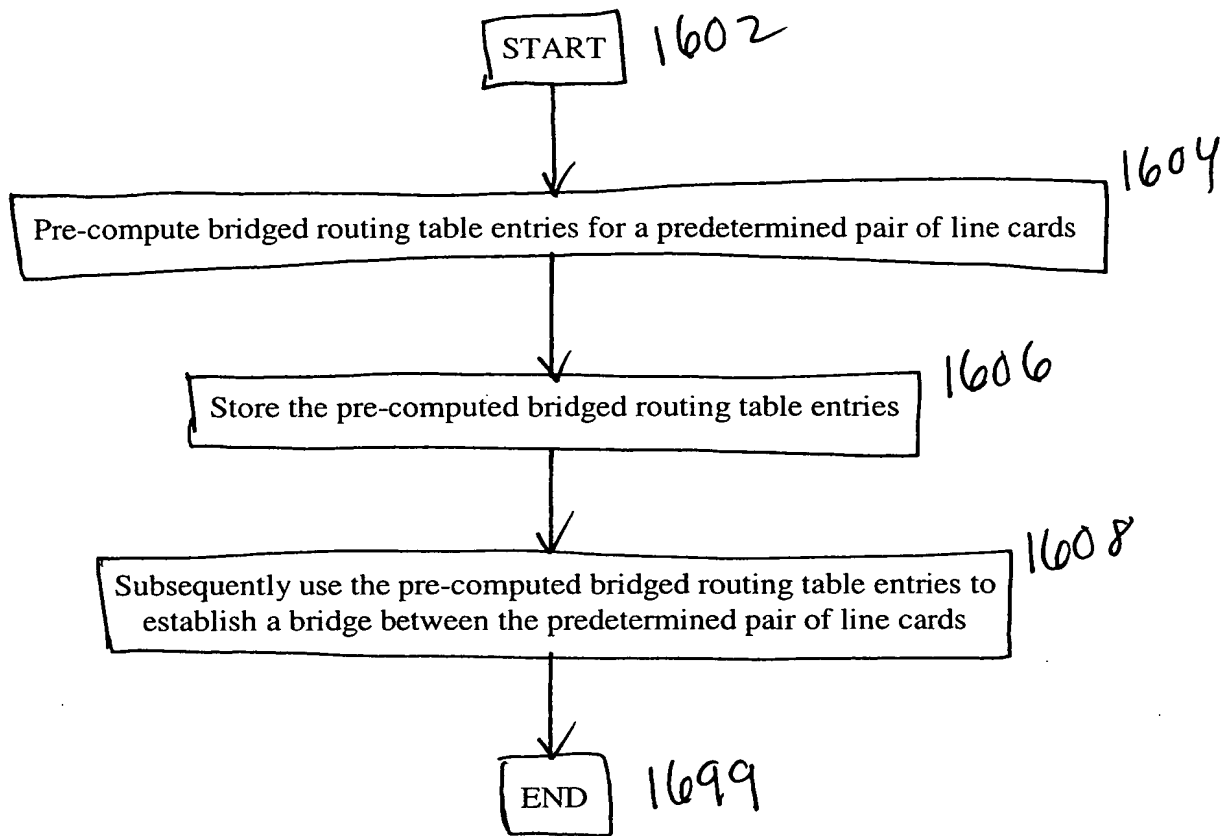


FIG. 16

1600

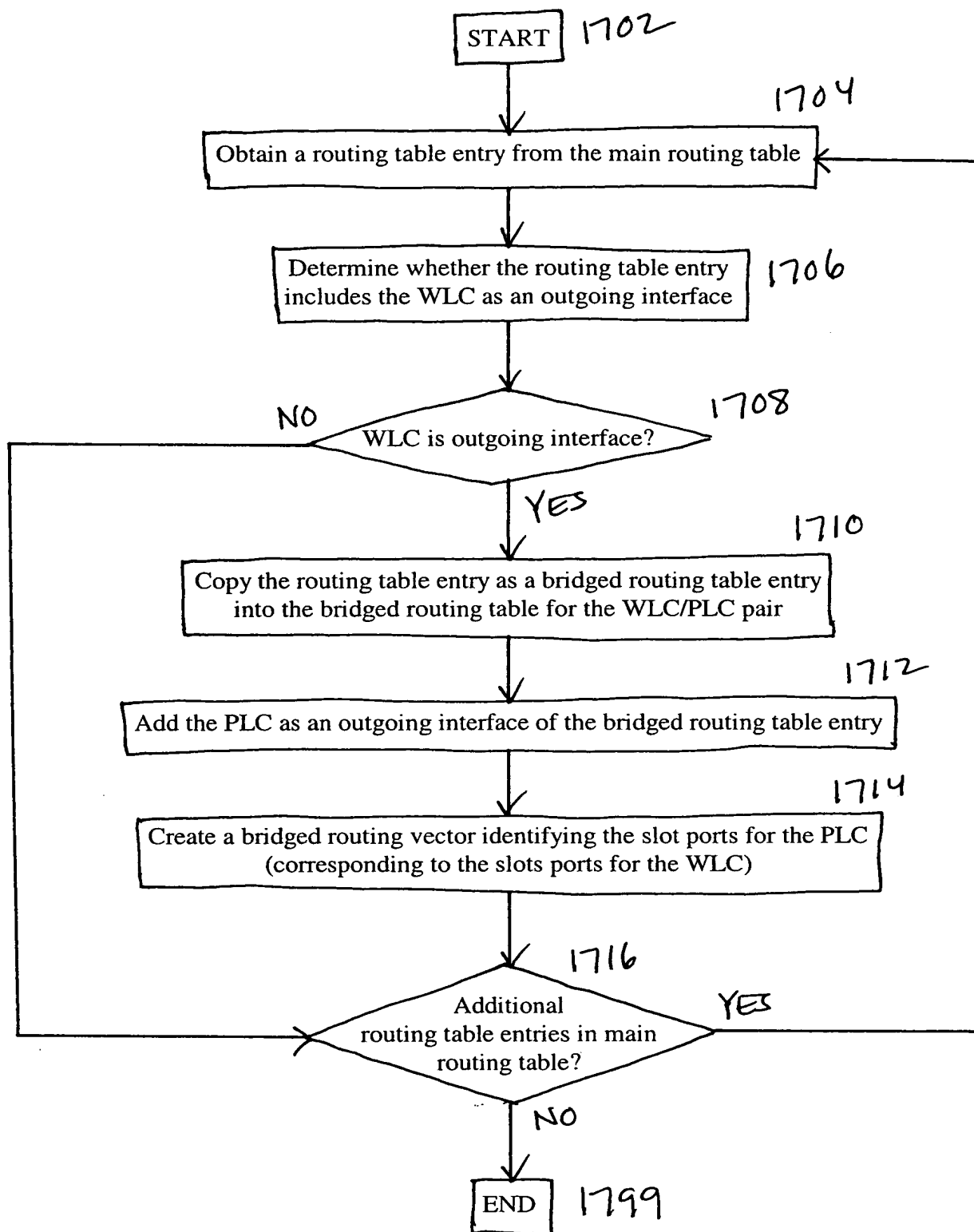


FIG. 17 1700

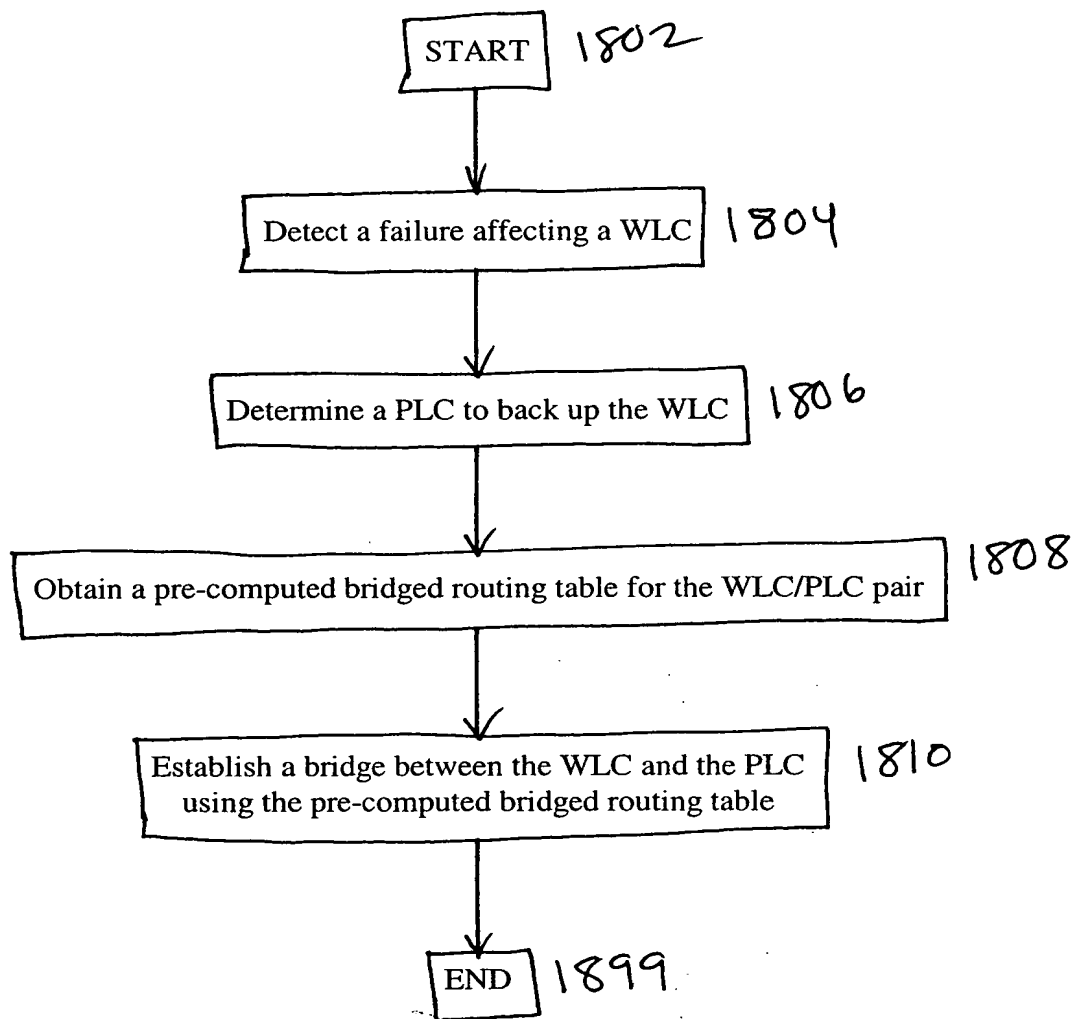


FIG. 18 1800